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On October 3, 2003

TOWNSEND and TOWNSEND and CREW LLP

By: Hana L. McNeill

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT
Attorney Docket No.: 016747-009910US
Client Reference No.: P2404/US

RECEIVED

NOV 12 2003

Technology Center 2100

In re application of:

Ashley Saulsbury, et al.

Application No.: 09/802,017

Filed: March 8, 2001

For: VLIW COMPUTER PROCESSING
ARCHITECTURE WITH ON-CHIP
DRAM USABLE AS PHYSICAL
MEMORY OR CACHE MEMORY

Examiner: Portka, Gary J.

Art Unit: 2188

FIFTH INFORMATION DISCLOSURE
STATEMENT UNDER 37 CFR §1.97 and
§1.98

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The references cited on attached form PTO/SB/08A and PTO/SB/08B are being called to the attention of the Examiner. Copies of the references are enclosed.

It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

As provided for by 37 CFR 1.97(g) and (h), no inference should be made that the information and references cited are prior art merely because they are in this statement and no

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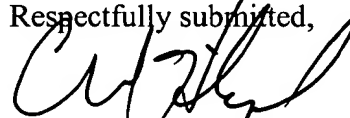
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representation is being made that a search has been conducted or that this statement encompasses all the possible relevant information.

This IDS is being filed before the mailing date of the final Office Action or Notice of Allowance.

Please charge the IDS fee of \$180 to Deposit Account No. 20-1430. Please deduct any additional fees from, or credit any overpayment to, the above-noted Deposit Account.

Respectfully submitted,



Chad S. Hilyard
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